

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))Docket No.
OKI.570

In Re Application Of: Toru MORI et al.

Serial No.
10/648,784Filing Date
August 27, 2003Examiner
To Be AssignedGroup Art Unit
2811

Title: LSI DEVICE AND MANUFACTURING METHOD OF THE ABOVE

Address to:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

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LSI DEVICE AND MANUFACTURING METHOD OF THE ABOVE

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
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Dated: NOVEMBER 25, 2003

 Signature

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INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

OKI.57()

Application Number

10/648,784

Applicant(s)

Toru MORI et al.

Filing Date

August 27, 2003

Group Art Unit

2811

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A	5,463,238	10/31/1995	TAKAHASHI et al.			
	B	5,574,292	11/12/1996	TAKAHASHI et al.			
	C	6,043,536	03/28/2000	NUMATA et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
	D	01-122154	05/15/1989	JAPAN			✓ Abstract	
	E	06-261265	10/18/1994	JAPAN			✓ Abstract	
	F	05-075041	03/26/1993	JAPAN			✓ Abstract	
	G	05243510 A	09/21/1993	JAPAN			✓ Abstract	

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ATTY DOCKET #: OKI.570

MAIL STOP MISSING PARTS

APPLICANT: Toru MORI et al.

DUE DATE: January 20, 2004

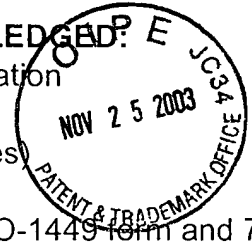
SERIAL NO.: 10/648,784

FILING DATE: August 27, 2003

TITLE: LSI DEVICE AND MANUFACTURING METHOD OF THE ABOVE

RECEIPT OF THE FOLLOWING PAPERS IS ACKNOWLEDGED:

- Response To Notice To File Missing Parts Of Application
- Copy of Notice
- Executed Declaration and Power of Attorney (8 pages)
- Claim of Priority including 1 priority document
- Transmittal of Information Disclosure Statement, PTO-1449 form and 7 references
- \$990.00 filing fee



DATE: November 25, 2003

ATTY: ACV

[Check No. 11182]